

Electronic Patent Application Fee Transmittal

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| Application Number: | 10538216 |
| Filing Date: | 09-Jun-2005 |
| Title of Invention: | VERTICAL INSULATED GATE TRANSISTOR AND MANUFACTURING METHOD |
| First Named Inventor/Applicant Name: | Jurriaan Schmitz |
| Filer: | Peter Zawilski |
| Attorney Docket Number: | NL02 1416 |

Filed as Large Entity

U.S. National Stage under 35 USC 371 Filing Fees

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
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| Basic Filing: | | | | |
| Pages: | | | | |
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| Miscellaneous-Filing: | | | | |
| Petition: | | | | |
| Patent-Appeals-and-Interference: | | | | |
| Post-Allowance-and-Post-Issuance: | | | | |
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